EENG 2980.003 - Digital Logic Design Lab
Spring 2018

Instructor: Dr. Colleen Bailey, NTDP B252, Colleen.Bailey@unt.edu

Office Hours: M 2:30 PM to 3:30 PM, Th 12:30 PM to 1:30 PM, or by appointment

Lab: W 5:30 PM to 8:20 PM, NTDP B207

TA: Puja Sah, NTDP B245, pujasah@my.unt.edu, TBD

Course Outline: TBD

Grading: TBD

Course Objectives: To understand analyze digital systems and the logical blocks that comprise them.

Blackboard: Course material and grades will be maintained on the course Blackboard site. You should check this page often to keep current on important information. https://learn.unt.edu

Rights and Responsibilities:

- Students aware of an authorized absence from a scheduled class or exam (religious observance, military service, official university function, etc.) should notify the instructor as soon as possible according to UNT Policy 15.2.5.

- Students with disabilities should inform the instructor of their needs at the beginning of the semester according to UNT Policy 18.1.14 in order to receive proper attention and accommodations.

- Cheating and academic dishonesty will not be tolerated. Any student found to have participated in academic dishonesty will receive an F in the class, and may be subject to further disciplinary action. Acts of academic dishonesty include: academic fraud (e.g. changing solutions to appeal a grade), copying or allowing one’s work to be copied, fabrication/falsification, plagiarism, sabotage of others’ work, substitution (e.g. taking an exam for someone else). For more details, see UNT Policy 18.1.16.

- Letter grades will not be assigned until the end of the term, after the final exam has been graded. Any letter grade assignment posted before the end of the class should be regarded as tentative and subject to change.