

EENG 5530- Analog Integrated Circuit Design (Spring 2018)

EENG 4010- Topics in EE (Spring 2018)

Class meetings B217 Tuesdays and Thursdays 11:30 am to 12:50 pm

Description

Thoroughly investigates the fundamentals in design and analysis of analog and mixed-signal integrated circuits. Topics include analog MOS transistor models, current sources and sinks, circuit reference, amplifier, feedback amplifiers, differential amplifiers and operational amplifiers.

Credit hours: 3 hrs.

Prerequisite(s): EENG 3520 or equivalent.

Instructor

Ifana Mahbub, Assistant Professor, Electrical Engineering Department

Office B208, Email Ifana.Mahbub@unt.edu Phone: 940-369-7027 Office hours: Tuesdays and Thursdays 2 to 3 pm or by appointment.

Teaching Assistant

Yuan Cao, Graduate Teaching Assistant, Office B251, Email yuanc@my.unt.edu Office hours: Monday, Wednesday and Friday: 11:30 am to 12: 30 pm or by appointment.

Format

- Lectures, notes would be provided (See suggested reference text books)
- Online: announcements, grades via Blackboard learn <https://learn.unt.edu>

Grade

Home works/Mini Assignments: 50%

Mid-term: 25%

Final Assignment: 25% (Report: 15%, Presentation: 10%)

Grade distribution

A=90-100, B=80-89, C=70-79, D=60-69, F=0-59

Schedules of exams

- Final Assignment Presentation days: May 1 and May 3, 2018
- Final Report Submission Deadline: May 4, 2018

Textbooks

Required: R Jacob Baker, *CMOS- Circuit Design, Layout, Simulation*, John Wiley and Sons, 3rd edition, 2010, ISBN 978-0-470- 88132-3.

Optional: Behzad Razavi, *Design of Analog CMOS Integrated Circuits*, McGraw-Hill, 2001, ISBN 0-07-238032-2.

Class Evaluation by Students

SPOT is a requirement for all organized classes at UNT and is available for your input at the end of the semester.

Course Learning Outcomes (CLO):

Upon successful completion of this course, the students will be able to:

1. Discuss the operation of a field-effect transistor in weak, moderate, and strong inversion and how it relates to SPICE parameters.
2. Describe the gain, speed, and matching trade-offs when setting the width, length, and overdrive of transistors.
3. Analyze and design transistor current mirrors, amplifiers, and differential amplifiers.
4. Design and analyze voltage and current references.
5. Design op-amps for specific gain, speed, or switching performance.
6. Analyze the frequency response of amplifier and operational amplifier circuits.
7. Compensate operational amplifiers for stability.

ABET Student Learning Outcomes (SO)

SO-1 Ability to apply mathematics, science and engineering principles.

SO-2 Ability to design and conduct experiments, analyze and interpret data.

SO-3 Ability to design a system, component, or process to meet desired needs.

SO-4 Ability to function on multidisciplinary teams.

SO-5 Ability to identify, formulate and solve engineering problems.

SO-6 Understanding of professional and ethical responsibility.

SO-7 Ability to communicate effectively.

SO-8 The broad education necessary to understand the impact of engineering solutions in a global and societal context.

SO-9 Recognition of the need for and an ability to engage in life-long learning.

SO-10 Knowledge of contemporary issues.

SO-11 Ability to use the techniques, skills and modern engineering tools necessary for engineering practice.

CLO	ABET Student Outcomes										
	SO-1	SO-2	SO-3	SO-4	SO-5	SO-6	SO-7	SO-8	SO-9	SO-10	SO-11
1	X	X	X			X	X	X	X	X	X
2			X			X	X	X	X		X
3						X	X	X		X	X
4	X	X	X			X	X	X	X	X	X
5			X				X	X	X	X	X
6	X	X	X				X	X	X	X	X
7						X	X	X	X	X	X

Policies

A. Academic Integrity Standards and Consequences. According to UNT Policy 06.003, Student Academic Integrity, academic dishonesty occurs when students engage in behaviors including, but not limited to cheating, fabrication, facilitating academic dishonesty, forgery, plagiarism, and sabotage. A finding of academic dishonesty may result in a range of academic penalties or

sanctions ranging from admonition to expulsion from the University. See full policy at <https://policy.unt.edu/sites/default/files/06.003.pdf>.

B. ADA Accommodation Statement. UNT makes reasonable academic accommodation for students with disabilities. Students seeking accommodation must first register with the Office of Disability Accommodation (ODA) to verify their eligibility. If a disability is verified, the ODA will provide a student with an accommodation letter to be delivered to faculty to begin a private discussion regarding one's specific course needs. Students may request accommodations at any time, however, ODA notices of accommodation should be provided as early as possible in the semester to avoid any delay in implementation. Note that students must obtain a new letter of accommodation for every semester and must meet with each faculty member prior to implementation in each class. For additional information see the ODA website at disability.unt.edu.

C. Course Safety Procedures (for Laboratory Courses). Students enrolled in [insert class name] are required to use proper safety procedures and guidelines as outlined in UNT Policy 06.038 Safety in Instructional Activities. While working in laboratory sessions, students are expected and required to identify and use proper safety guidelines in all activities requiring lifting, climbing, walking on slippery surfaces, using equipment and tools, handling chemical solutions and hot and cold products. Students should be aware that the UNT is not liable for injuries incurred while students are participating in class activities. All students are encouraged to secure adequate insurance coverage in the event of accidental injury. Students who do not have insurance coverage should consider Standard Syllabus Statements Related Policy 06.049 Course Syllabi Requirements obtaining Student Health Insurance. Brochures for student insurance are available in the UNT Student Health and Wellness Center. Students who are injured during class activities may seek medical attention at the Student Health and Wellness Center at rates that are reduced compared to other medical facilities. If students have an insurance plan other than Student Health Insurance at UNT, they should be sure that the plan covers treatment at this facility. If students choose not to go to the UNT Student Health and Wellness Center, they may be transported to an emergency room at a local hospital. Students are responsible for expenses incurred there.

D. Emergency Notification & Procedures. UNT uses a system called Eagle Alert to quickly notify students with critical information in the event of an emergency (i.e., severe weather, campus closing, and health and public safety emergencies like chemical spills, fires, or violence). In the event of a university closure, please refer to Blackboard for contingency plans for covering course materials.

E. Student Evaluation Administration Dates. Student feedback is important and an essential part of participation in this course. The student evaluation of instruction is a requirement for all organized classes at UNT. The survey will be made available during weeks 13, 14 and 15 of the long semesters to provide students with an opportunity to evaluate how this course is taught. Students will receive an email from "UNT SPOT Course Evaluations via IASystem Notification" (no-reply@iasystem.org) with the survey link. Students should look for the email in their UNT email inbox. Simply click on the link and complete the survey. Once students complete the survey

they will receive a confirmation email that the survey has been submitted. For additional information, please visit the SPOT website at www.spot.unt.edu or email spot@unt.edu.

Tentative Course Calendar

Week	Date	Topics	Homework/Mini Assignment
1	01/16	Introduction	
	01/18	MOS small-signal analysis, Norton drain, Thevenin source models, etc.	
2	01/23	MOS modeling from weak through strong inversion	1 (Due Date: 01/30)
	01/25	MOS modeling from weak through strong inversion	
3	01/30	No class	
	02/01	MOS resistors, switches and transmission gates	2 (Due Date: 02/08)
4	02/06	MOS resistors, switches and transmission gates	
	02/08	MOS Current mirrors-simple,cascode, LV cascode, regulated cascode	
5	02/13	MOS Current mirrors-simple,cascode, LV cascode, regulated cascode	3 (Due Date: 02/20)
	02/15	Band-gap reference and LDO	
6	02/20	Band-gap reference and LDO	
	02/22	MOS single-stage amplifier using Thevenin and Norton model	3 (Due Date: 03/01)
7	02/27	MOS single-stage amplifier using Thevenin and Norton model	
	03/01	Mid-term Exam review	
8	03/06	Mid-term Exam	
	03/08	MOS single-stage amplifier frequency response	4 (Due Date: 03/20)
9	03/13	Spring Break (no classes)	
	03/15	Spring Break (no classes)	

10	03/20	MOS single-stage amplifier frequency response	
	03/22	MOS differential amplifiers – differential, CM, PS “half circuits”	5 (Due Date: 03/29)
11	03/27	MOS differential amplifiers – differential, CM, PS “half circuits”	
	03/29	MOS single stage, dominant pole op amps (OTA) two-stage op amps	
12	04/03	MOS single stage, dominant pole op amps (OTA), two-stage op amps	5 (Due Date: 04/10)
	04/05	No Class	
13	04/10	MOS fully differential Op amps (Common mode feedback)	
	04/12	MOS fully differential Op amps (Common mode feedback)	6 (Due Date: 04/19)
14	04/17	Design review for final assignment design projects	
	04/19	Negative Feedback	
15	04/24	Negative Feedback	
	04/26	NonLinear Analog Circuits	
	05/01& 05/03	Final Assignment Presentation days	

Cadence Software Access:

The undergraduate and graduate students will be using the 0.5 micron CMOS process and eventually graduate students will also be using the 180nm CMOS process.

Lab computers where you can access Cadence:

You should be able to remotely log in to the Cadence server from the computers in the B227 lab. The lab is **unavailable** in the following hours:

Tuesday and Thursday 11:30 am – 12:50 pm
Monday and Wednesday 10 am – 11:20 am
Monday and Wednesday 1 pm – 2:20 pm